

AMENDMENTS TO THE CLAIMS

This listing of claims replaces all prior versions, and listings, of claims in the application:

What is claimed is:

1. (Currently Amended) A module for reading data carriers, with a processor arrangement and a reading unit,

wherein the module is designed for incorporation in a data processing device,

wherein addressable coded data are stored on the data carrier, and

wherein the processor arrangement comprises a decoding function and is for this purpose designed for

receiving a request, characterized by an identifier, for decoded data which are stored in coded form on the data carrier,

controlling the reading unit such that the requested data, defined by a start address, are read in the coded form from the data carrier,

converting the coded data into decoded data by means of the decoding function, and

sending ~~making~~ the decoded data, characterized by the identifier, to the data processing device available, wherein the identifier is used to distinguish between data belonging to the previous request and data belonging to the current request.

2. (Original) A module as claimed in claim 1, characterized in that the processor arrangement is designed for characterizing the decoded data by means of the current address from which the coded data were read from the data carrier.

3. (Original) A module as claimed in claim 1, characterized in that the processor arrangement is designed for receiving the start address immediately along with the request.

4. (Original) A module as claimed in claim 1, characterized in that the module comprises a memory arrangement,

wherein said memory arrangement is designed for storing table of contents information of the data carrier, and

wherein the processor unit is designed for deriving the start address from the request by using the table of contents information.

5. (Currently Amended) A module as claimed in claim 1, characterized in that the processor arrangement is designed for receiving the request characterized by an identifier via a first serial bus and for sending ~~making available~~ the decoded data characterized by the identifier to the data processing device via a second serial bus.

6. (Original) A module as claimed in claim 1, characterized in that the module comprises a memory arrangement, and in that the processor arrangement is designed for loading a decoding program from the memory arrangement, which program carries out the decoding function on a programmable processor.

7. (Original) A module as claimed in claim 1, characterized in that the data processing device is a car radio or a navigation system or a combined car radio/navigation system.

8. (Original) A data carrier playback device, in which a module according to claim 7 is incorporated.